OIPE
JAN 2 1 2005
PATENT & TRADEMARK OFFICE
JC161



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

CARL M. ELLISON, ET AL.

Application No.: 09/822,986

Filed: March 30, 2001

For: **Checking File Integrity Using Signature Generated In Isolated Execution**

Art Group: 2137

Examiner: Michael J. Pyzocha

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

01/26/2005 AWONDAF1 00000015 09822986
01 FC:1806 180.00 OP

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of $180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: January 19, 2005

William W. Schaal, Reg. No. 39,018

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (714) 557-3800

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*Susan McFarlane* — *01-19-05*

***Susan McFarlane*** — ***Date***

PTO/SB/08A(10-01)
Approved for use through 10/31/2002. OMB 851-0031
US Patent & Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

*(Use as many sheets as necessary)*

OIPE JC151 JAN 2 1 2005 PATENT & TRADEMARK OFFICE

Sheet 1 of 3

| *Complete if Known* | |
|---|---|
| **Application Number** | 09/822,986 |
| **Filing Date** | March 30, 2001 |
| **First Named Inventor** | Ellison, Carl |
| **Group Art Unit** | 2134 |
| **Examiner Name** | Morse, Gregory |

Attorney Docket No: 42P08096

## US PATENT DOCUMENTS

| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | Filing Date If Appropriate |
|---|---|---|---|---|---|---|
| | US-4,621,318 | 11/04/1986 | Maeda, A. | 364 | 200 | 02/01/1983 |
| | US-5,628,023 | 05/06/1997 | Bryant, Barbara J., et al. | 398 | 800 | 12/27/1994 |
| | US-5,944,821 | 08/31/1999 | Angelo, Michael F. | 364 | 200 | 07/11/1996 |

## FOREIGN PATENT DOCUMENTS

| Examiner Initials* | Foreign Document No | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | T[2] |
|---|---|---|---|---|---|---|
| | DE-DE4217444 | 12/03/1992 | Toyohisa, Imada , et al. | | | |
| | EP-0602867 | 06/22/1994 | Protheroe, Robert L., et al. | G06F | 12/14 | |
| | EP-EP0473913 | 03/11/1992 | Farrell, Joel A. | | | |
| | EP-EP0892521 | 01/20/1999 | Zamek, Steven | | | |
| | EP-EP1055989 | 11/29/2000 | Proudler, Graeme J., et al. | | | |
| | EP-EP1056014 | 11/29/2000 | Proudler, Graeme J., et al. | | | |
| | EP-EP1209563 | 05/29/2002 | England, Paul , et al. | G06F | 9/445 | |
| | WO-WO0201794 | 01/03/2002 | Ellison, Carl | | | |
| | WO-WO02060121 | 08/01/2002 | Grawrock, David W. | | | |
| | WO-WO03058412 | 07/17/2003 | Glew, Andrew , et al. | | | |
| | WO-WO9812620 | 03/26/1998 | Nishiuchi, Taiki , et al. | | | |
| | WO-WO9918511 | 04/15/1999 | Edrich, David R. | | | |

## OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No [1] | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T[2] |
|---|---|---|---|
| | | BRANDS, STEFAN , "Restrictive Blinding of Secret-Key Certificates", SPRINGER-VERLAG XP002201306, (1995),Chapter 3 | |
| | | COULOURIS, GEORGE , et al., "Distributed Systems, Concepts and Designs", 2nd Edition, (1994),422-424 | |
| | | CRAWFORD, JOHN , "Architecture of the Intel 80386", Proceedings of the IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD '86), (October 6, 1986),155-160 | |
| | | DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through Cryptographic Authentication", Proceedings of the Symposium on Security and Privacy, IEEE Comp. Soc. Press, ISBN 0-8186-1939-2,(May 1989), | |
| | | FABRY, R.S. , "Capability-Based Addressing", Fabry, R.S., "Capability-Based Addressing," Communications of the ACM, Vol. 17, No. 7, (July 1974),403-412 | |
| | | FRIEDER, GIDEON , "The Architecture And Operational Characteristics of the | |

**EXAMINER** **DATE CONSIDERED**



* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

PTO/SB/08A(10-01)
Approved for use through 10/31/2002. OMB 651-0031
US Patent & Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

*(Use as many sheets as necessary)*

OIPE JC161 JAN 2 1 2005 TRADEMARK OFFICE

| *Complete if Known* | |
|---|---|
| **Application Number** | 09/822,986 |
| **Filing Date** | March 30, 2001 |
| **First Named Inventor** | Ellison, Carl |
| **Group Art Unit** | 2134 |
| **Examiner Name** | Morse, Gregory |

Sheet 2 of 3

Attorney Docket No: 42P08096

## OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No [1] | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T [2] |
|---|---|---|---|
| | | VMX Host Machine", The Architecture And Operational Characteristics of the VMX Host Machine, IEEE, (1982),9-16 | |
| | | INTEL, "IA-32 Intel Architecture Software Developer's Manual", Volume 3: System Programming Guide, Intel Corporation - 2003,13-1 through 13-24 | |
| | | INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", Intel Product Specification, Order Number 245359-001, (01/2000),1-112 | |
| | | INTEL CORPORATION, "Intel 82802AB/82802AC Firmware Hub (FWH)", Intel Product Datasheet, Document Number 290658-004,(November 2000),1-6, 17-28 | |
| | | KASHIWAGI, KAZUHIKO , et al., "Design and Implementation of Dynamically Reconstructing System Software", Software Engineering Conference, Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996, Los Alamitos, CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8,(1996), | |
| | | LUKE, JAHN , et al., "Replacement Strategy for Aging Avionics Computers", IEEE AES Systems Magazine, XP002190614,(March 1999), | |
| | | MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", CRC Press Series on Discrete Mathematices and its Applications, Boca Raton, FL, XP002165287, ISBN 0849385237,(Oct. 1996),403-405, 506-515, 570 | |
| | | MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", CRC Press LLC, USA XP002201307, (1997),475 | |
| | | NANBA, S. , et al., "VM/4: ACOS-4 Virtual Machine Architecture", VM/4: ACOS-4 Virtual Machine Architecture, IEEE, (1985),171-178 | |
| | | RICHT, STEFAN , et al., "In-Circuit-Emulator Wird Echtzeittauglich", Elektronic, Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-103),8-6-1991 | |
| | | RSA SECURITY, "Hardware Authenticators", www.rsasecurity.com/node.asp?id=1158, 1-2 | |
| | | RSA SECURITY, "RSA SecurID Authenticators", www.rsasecurity.com/products/securid/datasheets/SID_DS_0103.pdf, 1-2 | |
| | | RSA SECURITY, "Software Authenticators", www.srasecurity.com/node.asp?id=1313, 1-2 | |
| | | SAEZ, SERGIO , et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP002190615,(July 1999),43-48 | |
| | | SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002939871; ISBN 0471117099,(Oct. 1995),47-52 | |
| | | SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", Wiley, John & Sons, Inc., XP002138607; ISBN 0471117099,(Oct. 1995),56-65 | |
| | | SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code C", Wiley, John & Sons, Inc., XP0021111449; ISBN | |

**EXAMINER** **DATE CONSIDERED**



* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. [1] Applicant's unique citation designation number (optional) [2] Applicant is to place a check mark here if English language Translation is attached

PTO/SB/08A(10-01)
Approved for use through 10/31/2002. OMB 651-0031
US Patent & Trademark Office: U.S. DEPARTMENT OF COMMERCE
Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

*(Use as many sheets as necessary)*

OIPE JC151 JAN 2 1 2005 & TRADEMARK OFFICE

Sheet 3 of 3

*Complete if Known*

| | |
|---|---|
| **Application Number** | 09/822,986 |
| **Filing Date** | March 30, 2001 |
| **First Named Inventor** | Ellison, Carl |
| **Group Art Unit** | 2134 |
| **Examiner Name** | Morse, Gregory |

Attorney Docket No: 42P08096

## OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials* | Cite No [1] | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T[2] |
|---|---|---|---|
| | | 0471117099,(Oct. 1995),169-187 | |
| | | SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; Wiley, John & Sons, Inc., XP002251738; ISBN 0471128457,(Nov. 1995),28-33; 176-177; 216-217; 461-473; 518-522 | |
| | | SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA, (Nov. 2001), | |

**EXAMINER** **DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached